

# Single phase inverter circuit diagram

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In this topic, you study Square Wave Inverter - Definition, Circuit Diagram & ...

This article explains Single Phase Full Bridge Inverter with the help of circuit diagram and various relevant waveforms. Comparison between half and full bridge inverters have also been detailed.

Single Phase Full Bridge Inverter is basically a voltage source inverter. Unlike Single Phase Half Bridge Inverter, this inverter does not require three wire DC input supply. Rather, two wire DC input power source suffices the requirement. The output frequency can be controlled by controlling the turn ON and turn OFF time of the thyristors.

The power circuit of a single phase full bridge inverter comprises of four thyristors T1 to T4, four diodes D1 to D4 and a two wire DC input power source  $V_s$ . Each diode is connected in antiparallel to the thyristors viz. D1 is connected in anti-parallel to T1 and so on. The power circuit diagram of a single phase full bridge inverter is shown in the figure below.

It may be noted that the circuitry for turning ON and turning OFF the thyristor is not shown in the above circuit diagram to maintain simplicity. Further, it is assumed that each of the thyristor only conducts for the period its gate signal is present and as soon as the gate signal is removed, the thyristors gets turned OFF.

The working principle of single phase full bridge inverter is based on the sequential triggering of thyristors placed diagonally opposite. This means, for half of time period, thyristors T3 & T4 will be triggered while for the remaining half of time period, T1 & T2 will be triggered. Only two thyristors are turned ON in half of the time period.

Carefully observe the waveform of the gating signal. You will notice that thyristors T1 & T2 are triggered simultaneously for a time  $T/2$ . Therefore, load is connected to source through T1 & T2 and hence, the load voltage is equal to the source voltage with positive polarity. This is the reason; the load voltage is shown positive & equal to  $V_s$  in the output voltage waveform.

As soon as the gate signal ( $ig1$  &  $ig2$ ) are removed, T1 and T2 get turned OFF. However, at the same instant gate signal ( $ig3$  &  $ig4$ ) are applied and hence, T3 & T4 are turned ON. When T3 & T4 are conducting, load gets connected to the source. The load voltage magnitude is again  $V_s$  but with reverse polarity. This is the reason, the output voltage is shown negative in the voltage waveform.

For the time  $0 \leq t \leq (T/2)$ , thyristors T1 & T2 conducts and load voltage  $V_o = V_s$ .

For the time  $(T/2) \leq t \leq T$ , thyristors T3 & T4 conducts and load voltage  $V_o = -V_s$ .

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I think you have understood the working principle of single-phase half bridge inverter. But I am sure that you might be thinking the purpose of diodes D1 to D4. I will explain.

If the load is purely resistive, there is no need to put diode D1 to D4 as the output voltage and current are always in phase. But unfortunately, for loads other than purely resistive, the load current ( $i_o$ ) will not be in phase with the load voltage ( $v_o$ ). For such case, the diode connected in anti-parallel with the thyristor will allow the current to flow when main thyristor is turned off. When these diode conducts, the energy is fed back to the DC source and hence, these diodes (D1 to D4) are called flyback diode.

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Web: <https://www.kary.com.pl/contact-us/>

Email: [energystorage2000@gmail.com](mailto:energystorage2000@gmail.com)

WhatsApp: 8613816583346

